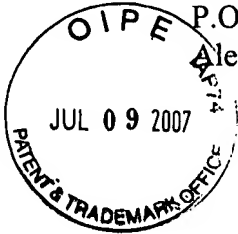


11m 1765

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

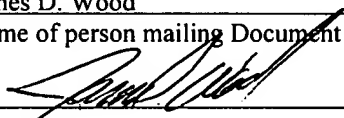


I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 5, 2007

(Date of Deposit)

James D. Wood

Name of person mailing Document or Fee


Signature

July 5, 2007

Date of Signature

Re:	Application of:	Charles E. May
	Serial No.:	09/943,196
	Filed:	August 30, 2001
	For:	Arrangement and Method for Fabricating A Semiconductor Wafer
	Group Art Unit:	1765
	Examiner:	Lynette T. Umez-Eronini
	Our Docket No.:	1003-0606

TRANSMITTAL OF RESPONSE TO OFFICE ACTION

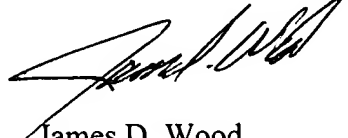
Please find for filing in connection with the above patent application the following:

1. Response to Office Action; and
2. One (1) return post card.

* The fee has been calculated as shown below.

CLAIMS AS AMENDED				
	Claims Remaining After Amendment	Highest No. Paid For	Fee Calculation	Addit Fee
Total Claims	15	20	0 X 50	\$ 0.00
Independent Claims	3	3	0 X 200	\$ 0.00
Total Additional Fee Required				\$ 0.00

Respectfully Submitted,
MAGINOT, MOORE & BECK LLP



James D. Wood
Registration No. 43,285
Chase Tower
111 Monument Circle, Suite 3250
Indianapolis, Indiana 46204-5109

July 5, 2007

Enclosures



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited
with the United States Postal Service with sufficient postage
as first class mail in an envelope addressed to: Mail Stop
Amendment, Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450 on July 5, 2007

(Date of Deposit)

James D. Wood

Name of person mailing Document or Fee


Signature

July 5, 2007

Date of Signature

Re:	Application of:	Charles E. May
	Serial No.:	09/943,196
	Filed:	August 30, 2001
	For:	Arrangement and Method for Fabricating A Semiconductor Wafer
	Group Art Unit:	1765
	Examiner:	Lynette T. Umez-Eronini
	Confirmation No.:	8500
	Our Docket No.:	01-146/RCE (1003-0606)

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated April 5, 2007, please enter the following:

Amendments to the claims begin on page 2 of this paper; and

Remarks/Arguments begin on page 7 of this paper.